Second Laureate
Applied Research

Project Title: Design and implement AUT Cluster system

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Abstract:

The knowledge acquisition in design and implementation of cluster is a crucial achievement which plays a key role in development of strategic technical and scientific products. In the other word, many high-tech research areas have great affinity to powerful computational engines which one can overcome the difficulties of modeling, implementation, simulation, validation, and verification of complicated scientific problems.

With 15 years of research and experience in devising/developing different cluster computers, the HPCRC center of AUT is pleased to announce its successful achievements in different dimensions including: System Architecture, Workload Management, Single System Image, Parallel I/O, High Availability, System Security, Operating System, Hardware Management, and Parallel Programming Tools. The number of publications in prominent scientific journals and well-known conferences clearly illustrate the volume and importance of works during this period.